2004/04/14	USPAT	-flop\$1 "D-flipflop" "D-flip-flop" "flipflop" "d flipsmi\$5 same receiv\$5 same (separat\$5 control\$2 timing\$1er\$2))	208377	19
2004/04/14	USPAT	"D-flipflop" "D-flip-flop" "Hipflop" "d flip flop") ((((time\$2 timing\$2 separation\$2) near9 (control\$2 generat sens\$5 difference\$5 chatter\$5)) (flip-flop\$1 "D-flipflop" "flipflop" "gripflop" "d flip flop")).ab.) and (342/124,175,205.ccls 290V.ccls.) (324/644.ccls. 73/304R,304c, 305-322, 322.5.ccl	27	17
2004/04/14	USPAT	"D-flipflop" "D-flip-flop" "flipflop" "d flip flop") ((((time\$2 timing\$2 separation\$2) near9 (control\$2 generat sens\$5 difference\$5 chatter\$5)) (flip-flop\$1 "D-flipflop" "flinflop" "d flip flop") ah and 342/124 ccls) and (f	ത	18
2004/04/14	USPAT	"D-flipflop" "D-flip-flop" "f ((((time\$2 timing\$2 separatio sens\$5 difference\$5 chatter\$5 "flipflop" "A flop") an	11	16
2004/04/14	USPAT	"flipflop" "d flip flop")).ab.) and 342/124.ccls. ((((time\$2 timing\$2 separation\$2) near9 (control\$2 generat\$ sens\$5 difference\$5 chatter\$5)) (flip-flop\$1 "D-flipflop" " "flipflop" "d flip flop")) and (342/124,175,205.ccls. 73/29 "flipflop" "d flip flop")) and (342/124,175,205.ccls. 73/29	82	15
2004/04/14	USPAT	"Ilipilop" "d Ilip Ilop")).ab.) and (342/124,1/5,205.cc1s. /3/290K, 290V.cc1s.) (324/644.cc1s. 73/304R,304c, 305-322, 322.5.cc1s.) ((((time\$2 timing\$2 separation\$2) near9 (control\$2 generat\$5 detect\$5 sens\$5 difference\$5 chatter\$5)) (flip-flop\$1 "D-flipflop" "D-flip-flop"	20	14
2004/04/14	USPAT	\$2 generat\$5 flipflop" "D-	142	13
2004/04/14	USPAT	.) 2 generat\$5 flipflop" "	8 4	12
2004/04/14	USPAT	flop")).ab. separation\$2) near9 (control: chatter\$5)) (flip-flop\$1 "D- flop")) and (342/124,175,205	787	11
2004/04/14	USPAT	(((time\$2 timing\$2 separation\$2) near9 (control\$2 generat\$5 detect\$5 sens\$5 difference\$5 chatter\$5)) (flip-flop\$1 "D-flipflop" "D-flip-flop"	66197	10
2004/04/14	USPAT	342/124.ccls. ((time\$2 ctiming\$2 sepail ((difference\$5 chatter\$5)	123 679279	ν α
2004/04/14 2004/04/14	USPAT	324/644. (((timin (flip-fl (342/124	201 34	7
	USPAT	(flip-flop\$1 "D-flipflop" "D-flip-flop" "flipflop" "d flip flop") 342/124,175,205.ccls. 73/290R, 290V.ccls.) (324/644.ccls. 73/304R,	93	Cī
Time stamp 2004/04/14	DB	Search Text Search Text	Hits 31602	L Number

C:\APPS\EAST\workspaces\10687927.wsp

1 1 1 1	ı	1 .	31	30	29	28	27	20	25	21	20
1 0 1764 1152 138813		2	27	17	629	9583	4946	59293	14	4946	59293
6300897.URPN. 6628229.URPN. 342/124,175,205.ccls. 73/290R, 290V.ccls. 73/290R, 290V.ccls. ((timing\$2 separation\$2) near9 (circuit\$5 generat\$5 filp-flop\$2))	("6300897").PN.	("6700530").PN.	"D-flip-flop" "flipflop" "d flip flop") and (342/124,175,205.ccls. 73/290R, 290V.ccls.) (324/644.ccls. 73/304R,304c, 305-322, 322.5.ccls.) (("6137738") or ("6130637") or ("6107957") or ("6087978") or ("6072427") or ("5851083") or ("5701006") or ("5672957") or ("5659321") or ("5651286") or ("5563605") or ("4847623") or ("4737791") or ("4044353")).PN.	"D-Illp-Ilop" "IllpIlop" "d Illp Ilop") (((((transmi\$5 same receiv\$5 same (separat\$5 control\$2 timing\$1 time\$2 chatter\$2))) and (((sens\$5 detect\$5 level\$2 radar\$2 determin\$5) near5 (level\$2 gaug\$5)).ab.)) and ((transmi\$5 same receiv\$5 same (separat\$5 (level\$2 timing\$1 time\$2 chatter\$2)))) and (flip-flop\$1 "D-flipflop"	((((transmi\$5 same receiv\$5 same (separat\$5 control\$2 timing\$1 time\$2 chatter\$2))) and ((sens\$5 detect\$5 level\$2 radar\$2 determin\$5) near5 (level\$2 gaug\$5)).ab.)) and ((transmi\$5 same receiv\$5 same (separat\$5 control\$2 timing\$1 time\$2 chatter\$2)))) and (flip-flop\$1 "D-flipflop"	((((transmi\$5 same receiv\$5 same (separat\$5 control\$2 timing\$1 time\$2 chatter\$2))) and ((sens\$5 detect\$5 level\$2 radar\$2 determin\$5) near5 (level\$2 gaug\$5))) and ((transmi\$5 same receiv\$5 same (separat\$5 control\$2 timing\$1 time\$2 chatter\$2)))) and (flip-flop\$1 "D-flipflop" "h-flip-flop" "flipflop" "d flip flop")	(((transmi\$5 same receiv\$5 same (separat\$5 control\$2 timing\$1 time\$2 chatter\$2))) and (((sens\$5 detect\$5 level\$2 radar\$2 determin\$5) near5 (level\$2 gaug\$5)).and ((transmi\$5 same receiv\$5 same (separat\$5 (level\$2 radar\$2 determin\$5)) and (transmi\$5 same receiv\$5 same (separat\$5 chatter\$2)	(((transmi\$5 same receiv\$5 same (separat\$5 control\$2 timing\$1 time\$2 chatter\$2))) and ((sens\$5 detect\$5 level\$2 radar\$2 determin\$5) near5 (level\$2 gaug\$5))) and ((transmi\$5 same receiv\$5 same (separat\$5 control\$2 timing\$1 time\$2 chatter\$2)))	(level\$2 gaug\$5).ab.) ("4044353" "4737791" "4847623" "5563605" "5651286" "5659321" "5672975" "5701006" "5851083" "6072427" "6087978" "6107957"	ransmi\$5 : tter\$2)	2 ×
IBM_TDB USPAT USPAT USPAT USPAT USPAT USPAT	DERWENT; IBM_TDB USPAT; USPAT; EPO; JPO; DERWENT:	DERWENT; IBM_TDB USPAT; US-PGPUB; EDO: IDO:	USPAT; US-PGPUB; EPO; JPO;	USPAT	USPAT	USPAT	USPAT	USPAT	USPAT	USPAT	USPAT
2004/04/13 2004/04/13 2004/04/13 2004/04/13 2004/04/13 2004/04/14	2004/04/13	2004/04/13	2004/04/14	2004/04/14	2004/04/14	2004/04/14	2004/04/14	2004/04/14	2004/04/14	2004/04/14	2004/04/14
15:37 15:31 15:41 15:41 15:41 09:52	15:32	14:45	11:36	11:34	11:14	11:14	11:13	11:13	11:11	11:13	11:13

52 (((timing\$2 separation\$2) near9 (circuit\$5 generat\$5 filp-flop\$2))) and (filp-flop\$2) TAGSU 2004/04/14 09:54

Membership Publications/Services Standards Conferences Careers/Jobs

United States Patent and Trademark Office



» Search Results

			٨.				
***	J	***		90	HO.	re	
						e .	

lp	FAQ	Terms	IEEE Peer Review	Quick Links
				3

Welcome to IEEE Aphares ()- Home)- What Can I Access?

Tables of Contents

()- Log-out

Journals & Magazines

Conference Proceedings

Standards

Search

O- By Author

O- Basic

— Advanced

Hember Services

Or Join IEEE

Establish IEEE Web Account

O- Access the **IEEE Member** Digital Library Your search matched 41 of 1024576 documents.

A maximum of 500 results are displayed, 15 to a page, sorted by Relevance in **Descending** order.

Welcome

Refine This Search:

You may refine your search by editing the current search expression or entering a new one in the text box.

flip flop and (detect* or sens* or determin*) and (level* o

Search

☐ Check to search within this result set

Results Key:

JNL = Journal or Magazine CNF = Conference STD = Standard

1 Storage optimization by replacing some flip-flops with latches

Tsung-Yi Wu; Youn-Long Lin;

Design Automation Conference, 1996, with EURO-VHDL '96 and Exhibition, Proceedings EURO-DAC '96, European, 16-20 Sept. 1996

Pages: 296 - 301

[Abstract] [PDF Full-Text (636 KB)] **IEEE CNF**

2 Array of sensors with A/D conversion based on flip-flops

Lian, W.; Wouters, S.E.;

Instrumentation and Measurement, IEEE Transactions on , Volume: 39 , Issue:

4, Aug. 1990

Pages:653 - 657

[Abstract] [PDF Full-Text (432 KB)] **IEEE JNL**

3 Maximum operating frequency in Si bipolar master-slave toggle flip-flop circuit

Ishii, K.; Ichino, H.; Yamaguchi, C.;

Solid-State Circuits, IEEE Journal of , Volume: 29 , Issue: 7 , July 1994

Pages: 754 - 760

[PDF Full-Text (504 KB)] [Abstract] **IEEE JNL**

4 Partial scan design based on levelised combinational structure

Park, S.; Lee, G.;

Computers and Digital Techniques, IEE Proceedings-, Volume: 145, Issue: 4, July 1998

Pages:249 - 254

[Abstract] [PDF Full-Text (552 KB)] **IEE JNL**

5 Configuring flip-flops to BIST registers

Stroele, A.P.; Wunderlich, H.-J.;

Test Conference, 1994. Proceedings., International, 2-6 Oct. 1994

Pages:939 - 948

6 Computing optimal clock schedules

Szymanski, T.G.;

Design Automation Conference, 1992. Proceedings., 29th ACM/IEEE , 8-12 June 1992

Pages:399 - 404

[Abstract] [PDF Full-Text (484 KB)] IEEE CNF

7 Linearization of the timing analysis and optimization of level-sensitive digital synchronous circuits

Taskin, B.; Kourtev, I.S.;

Very Large Scale Integration (VLSI) Systems, IEEE Transactions on , Volume:

12 , Issue: 1 , Jan. 2004

Pages:12 - 27

[Abstract] [PDF Full-Text (464 KB)] IEEE JNL

8 Fast and energy-frugal deterministic test through test vector correlation exploitation

Sinanoglu, O.; Orailoglu, A.;

Defect and Fault Tolerance in VLSI Systems, 2002. DFT 2002. Proceedings. 17th IEEE International Symposium on , 6-8 Nov. 2002

Pages:325 - 333

[Abstract] [PDF Full-Text (279 KB)] IEEE CNF

9 On the use of VHDL simulation and emulation to derive error rates

Lima, F.; Rezgui, S.; Carro, L.; Velazco, R.; Reis, R.;

Radiation and Its Effects on Components and Systems, 2001. 6th European Conference on 10-14 Sort 2001

Conference on , 10-14 Sept. 2001

Pages: 253 - 260

[Abstract] [PDF Full-Text (598 KB)] IEEE CNF

10 Optimal design of synchronous circuits using software pipelining techniques

Boyer, F.-R.; Aboulhamid, El.M.; Savaria, Y.; Bennour, I.E.;

Computer Design: VLSI in Computers and Processors, 1998. ICCD '98.

Proceedings., International Conference on , 5-7 Oct. 1998

Pages:62 - 67

[Abstract] [PDF Full-Text (176 KB)] IEEE CNF

11 Built-in self testing of sequential circuits using precomputed test sets

Iyengar, V.; Chakrabarty, K.; Murray, B.T.;

VLSI Test Symposium, 1998. Proceedings. 16th IEEE, 26-30 April 1998.

Pages:418 - 423

[Abstract] [PDF Full-Text (224 KB)] IEEE CNF

12 Fixed-phase retiming for low power design

Lalgudi, K.N.; Papaefthymiou, M.C.;

Low Power Electronics and Design, 1996., International Symposium on , 12-14

Aug. 1996

Pages:259 - 264

[Abstract] [PDF Full-Text (560 KB)] IEEE CNF

13 A MOdular and Reprogrammable Real-time Processing Hardware, MORRPH

Drayer, T.H.; King, W.E., IV.; Tront, J.G.; Conners, R.W.; FPGAs for Custom Computing Machines, 1995. Proceedings. IEEE Symposium on , 19-21 April 1995 Pages:11 - 19

[Abstract] [PDF Full-Text (976 KB)] IEEE CNF

14 Property-based test generation for scan designs and the effects of the test application scheme and scan selection on the number of detectable faults

Pomeranz, I.; Reddy, S.M.;

Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions

on , Volume: 21 , Issue: 5 , May 2002

Pages:628 - 637

[Abstract] [PDF Full-Text (362 KB)] IEEE JNL

15 Active timing multilevel fault-simulation with switch-level accuracy

Meyer, W.; Camposano, R.;

Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions

on , Volume: 14 , Issue: 10 , Oct. 1995

Pages: 1241 - 1256

[Abstract] [PDF Full-Text (1504 KB)] IEEE JNL

1 2 3 Next

Home | Log-out | Journals | Conference Proceedings | Standards | Search by Author | Basic Search | Advanced Search | Join IEEE | Web Account | New this week | OPAC Linking Information | Your Feedback | Technical Support | Email Alerting | No Robots Please | Release Notes | IEEE Online Publications | Help | FAQ | Terms | Back to Top

Copyright © 2004 IEEE - All rights reserved